

As a below named inventor, I declare:
that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE AND ITS MANUFACTURING METHOD

the specification of which is attached hereto unless the following box is checked.

was filed on _____ as United States Application
or PCT International Application No. _____, and
was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

<u>Country</u>	<u>Category</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Priority Claim</u>
Japan	Patent	9-184863	July 10, 1997	Yes

And I hereby appoint Stuart Lubitz (Reg. No. 20,680), Richard H. Zaitlen (Reg. No. 27,248), Alex Chartove (Reg. No. 31,942), David M. Simon (Reg. No. 32,756), Jay M. Finkelstein (Reg. No. 21,082), Roger R. Wise (Reg. No. 31,204), William K. Konrad (Reg. No. 28,868), Louis A. Mok (Reg. No. 22,585), John P. Scherlacher (Reg. No. 23,009), Gary D. Mann (Reg. No. 34,867), Don F. Livornese (Reg. No. 32,040), Ted Rittmaster (Reg. No. 32,933), Paul H. Kovelman (Reg. No. 35,228), David L. Lubitz (Reg. No. 38,229), Paul G. Nagy (Reg. No. 37,896), Weining Yang (Reg. No. 38,690), Chris P. Perque (Reg. No. 37,696), Joseph F. Vonsauers (Reg. No. 27,253), Philippe O. Erwin (Reg. No. 38,132), David W. Victor (Reg. No. 39,867), Jonathan Y. Kang (Reg. No. 38,199) and Randall C. Furlong (Reg. No. 35,144), each of whose address is 10100 Santa Monica Boulevard, Suite 2200, Los Angeles, California 90067-4164, or anyone of them my attorneys with full power of substitution and revocation to prosecute this application and to transact all business in the Patent & Trademark Office connected therewith, and request that correspondence be directed to Loeb & Loeb LLP, 10100 Santa Monica Boulevard, Suite 2200, Los Angeles, California 90067-4164.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States

DECLARATION FOR PATENT APPLICATION

I declare further that my post office address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

<u>Inventor: (Signature)</u>	<u>Date</u>	<u>Residence</u>
<u>Toshitake Yaegashi</u>	<u>Date:</u> JUL - 6, 1998	<u>Citizen of:</u> Japan <u>Yokohama-shi, Japan</u>
<u>Kazuhiro Shimizu</u>	<u>Date:</u> JUL - 6, 1998	<u>Citizen of:</u> Japan <u>Yokohama-shi, Japan</u>
<u>Seiichi Arimoto</u>	<u>Date:</u> JUL - 6, 1998	<u>Citizen of:</u> Japan <u>Yokohama-shi, Japan</u>
	<u>Date:</u> _____	<u>Citizen of:</u> Japan _____
	<u>Date:</u> _____	<u>Citizen of:</u> Japan _____
	<u>Date:</u> _____	<u>Citizen of:</u> Japan _____
	<u>Date:</u> _____	<u>Citizen of:</u> Japan _____
	<u>Date:</u> _____	<u>Citizen of:</u> Japan _____

PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Toshitake YAEGASHI, et al.

Serial No: Not Assigned (Parent 09/741,261)

Filed: May 3, 2002

For: Non-Volatile Semiconductor Memory
Device and Its Manufacturing Method

Art Unit: Not Assigned

Examiner: Not Assigned

ASSOCIATE POWER OF ATTORNEY

(37 C.F.R. § 1.34)

Box PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Please recognize the following as my associate attorneys in the above-entitled application:

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Please associate this case with this firm's customer number:

Customer Number

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PATENT TRADEMARK OFFICE

Respectfully submitted,

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